# DC/DC Converter SK12MT-6A Series



Non-isolated & regulated 6A single output POL power converter



# **FEATURES**

- High efficiency up to 94%
- Wide input voltage range: 4.5VDC-14.4VDC
- Adjustable output voltage: 0.6VDC-5.5VDC
- Operating ambient temperature range: -40°C to +85℃
- Output short-circuit protection
- High-speed transient response
- Compact SMD package: 12.20 x 12.20 x 8.70mm
- SENSE, TRIM, PGOOD

SK12MT-6A series is a 6A non-isolated switching regulator. The output voltage is accurately adjustable from 0.6V to 5.5V, and the product is featured with high efficiency, fast transient response, output short-circuit protection. They are widely used in applications such as communications, computer network industry, power distributed architecture, workstations, servers, LANs/WANs and provide high current with fast transient response for high-speed chips such as FPGA, DSP, and ASIC.

Selection	n Guide							
		Input Voltage	e (VDC)	Out	put	Efficiency(%)	Capacitive	
Certification	ification Part No. <sup>™</sup> Nominal (Range)		Max.®	Voltage(VDC) <sup>®</sup> (Range)	Current (A) Max./Min.	Min./Typ.	Load(µF) Max.	
	SK12MT-6A-P(N)	12 (4.5-14.4)	15	0.6-5.5	6/0	91/94	1000	

Notes: (1) "P" indicates that the ON/OFF pin is positive logic control; "N" indicates that the ON/OFF pin is negative logic control;

- ② Exceeding the maximum input voltage may cause permanent damage;
- 3 The default output voltage is 0.6VDC, which can be adjusted to 1.2VDC, 1.8VDC, 2.5VDC, 3.3VDC, 5VDC. See Trim instructions for specific output voltage adjustment;
- 4 Unless otherwise specified, parameters in this table were measured under the 5VDC output voltage.

Item	Operating C	conditions	Min.	Тур.	Max.	Unit		
Input Current (full load/no-load)	Nominal inp	ut voltage		2660/1		mA		
Start-up Voltage <sup>®</sup>					4.5	VDC		
Reverse Polarity Input		Avoid / Not protected						
Hot Plug				Unavailable				
Input Filter		Capacitance filter						
		SK12MT-6A-P (Positive logic)	ON/OFF pin open or pulled high (3VDC ~ Vin)					
	Module on	SK12MT-6A-N (Negative logic)	ON/OFF pin		pulled low to GND (-0.2VDC)			
ON/OFF <sup>®</sup>	Module off	SK12MT-6A-P(Positive logic)	ON/OFF pin	ON/OFF pin pulled low to GND (-0.2VDC ~ 0.2VDC)				
	IVIOQUIE OII	SK12MT-6A-N (Negative logic)		ON/OFF pin pulled high (3VDC ~ Vin)				
	Input current	when off		1 m				

Output Specifications									
Item	Operating Conditions		Min.	Тур.	Max.	Unit			
Voltage Accuracy	Input voltage range,	TRIM resistor with 0.1% tolerance		-	±1	Q/			
	0% -100% load	TRIM resistor with 1% tolerance		-	±3	<b>%</b>			
Linear Regulation	Full load, input voltage	Vout≥2.5VDC			±0.4	%			
	range	Vout<2.5VDC			±5	mV			

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	Nominal input voltage,	Vout<5VDC		-	±10	
Load Regulation	10% -100% load	Vout≥5VDC		_	±20	mV
Ripple & Noise <sup>®</sup>	20MHz bandwidth, nomin	20MHz bandwidth, nominal input voltage, 10%-100% load		50	100	mVp-p
Trim			0.6		5.5	VDC
Sense					0.5	V
		Vout=0.6VDC		. 15		
		Co=2*47µF + 4*330µF		±15	<b></b>	
		Vout=1.2VDC		.05		-
		Co=47µF + 3*330µF		±25		
		Vout=1.8VDC		. 50		-
Translant Dansana Davidetta	Nominal input voltage,	Co=47µF + 330µF		±50		mV
Transient Response Deviation	50%-100%-50% load step change, di/dt=2.5A/us	Vout=2.5VDC		±65		
		Co=3*47µF				
		Vout=3.3VDC		70		
		Co=3*47µF		±70	<del></del>	
		Vout=5VDC		100		
		Co=2*47µF		±120	<del></del>	
Short-circuit Protection	Nominal input voltage		Re-po	wer on or ON,	OFF reset to re	ecover
Temperature Coefficient	100% load				±0.4	%/℃

② Unless otherwise specified, parameters in this table were measured under the 5VDC output voltage

General Specification	ns					
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Operating Temperature	See Fig. 1	-40		+85	°C	
Storage Temperature		-55		+125		
Storage Humidity	Non-condensing	5	%RH			
Reflow Soldering Temperature		Peak temp. To $\leq$ 245°C, maximum duration time $\leq$ 60s over 217°C. For actual application, please refer to IPC/JEDEC J-STD-020D.1.				
Vibration		10-150Hz, 5G, 0.75mm. along X, Y and Z				
Switching Frequency	Full load, nominal input voltage		500		kHz	
MTBF	MIL-HDBK-217F@25℃	18595			k hours	

Mechanical Specifications				
Dimensions	12.20 x 12.20 x 8.70mm			
Weight	2.5g (Typ.)			
Cooling Method	Nature convection			

# Typical Characteristic Curves



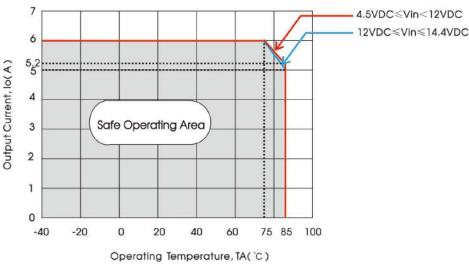
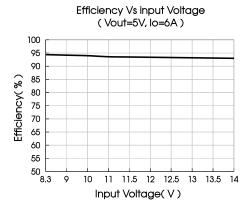
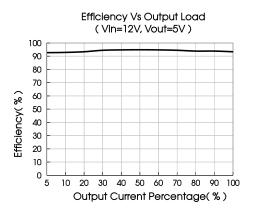


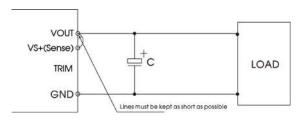
Fig. 1





# **Remote Sense Application**

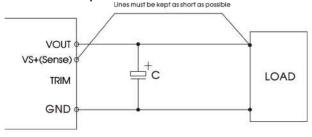
### 1. Remote sense connection if not used



#### Notes:

- 1. If the sense function is not used for remote regulation the user must connect the Sense to Vout at the DC-DC converter pins and will compensate for voltage drop across pins only;
- 2. The connections between Sense and Vout must be kept as short as possible, otherwise they may be picking up noise, interference and/or causing unstable operation of the power module.

### 2. Remote sense connection used for compensation



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#### Notes:

- 1. Using remote sense with long wires may cause unstable operation, please contact technical support if long wires must be used;
- 2. We recommend using adequate cross section for PCB-track layout and/or cables to connect the power supply module to the load in order to keep the voltage drop below 0.5V and to make sure the power supply's output voltage remains within the specified range;
- 3. Note that large wire impedance may cause oscillation of the output voltage and/or increased ripple. Consult technical support or factory for further advice of sense operation.

# PGOOD Application

## PGOOD recommended circuit

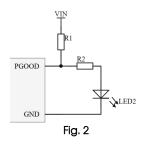


Table 1							
R1	<b>100k</b> Ω						
R2	Selecting based on LED2's current in application						

#### Notes:

PGOOD is the power good detection pin. When the product is working normally, PGOOD at a high level, and LED2 on; when the product is abnormal, which means the voltage on the FB pin is not within ±10% of the 0.6V, PGOOD is pulled to ground, and LED2 off.

## Design Reference

#### 1. Typical application

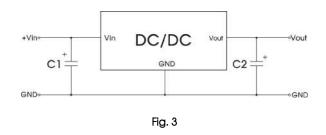


Table 2

Pa	Part No.		C2
	Vout=0.6V		4*330µF
	Vout=1.2V		2*330µF
SK12MT-6A-	Vout=1.8V	100	1*330µF
P(N)	Vout=2.5V	100µF/35V	3*47µF
	Vout=3.3V		3*47µF
	Vout=5V		2*47µF

### Notes:

- 1. The required capacitors C1 and C2 must be connected as close as possible to the terminals of the module, to ensure the stability of the converter:
- 2. To reduce the output ripple furtherly, increased values and/or tantalum or low ESR polymer capacitors may also be used instead;
- 3. Refer to Table 2 for C1 and C2 capacitor values;
- 4. Converter cannot be used for hot swap and with output in parallel.

#### 2. Trim function for output voltage adjustment (open if unused)

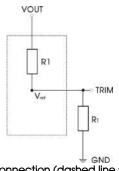


Fig. 4 TRIM resistor connection (dashed line shows internal resistor network)

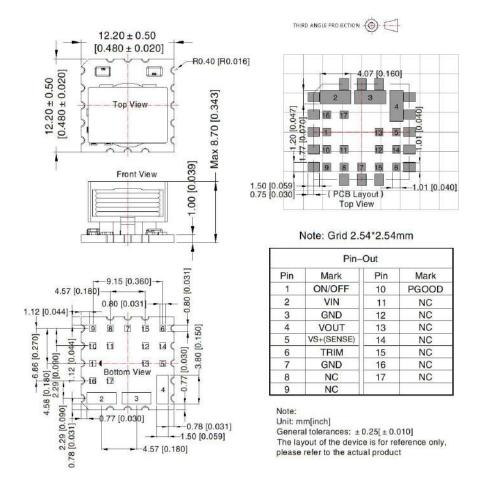
Tal	Table 3						
VOUT (VDC)	$R_T(k\Omega)$						
0.6	Open						
1.2	20						
1.8	10						
2.5	6.316						
3.3	4.444						
5	2.727						

Calculating Trim resistor (R<sub>T</sub>) values::

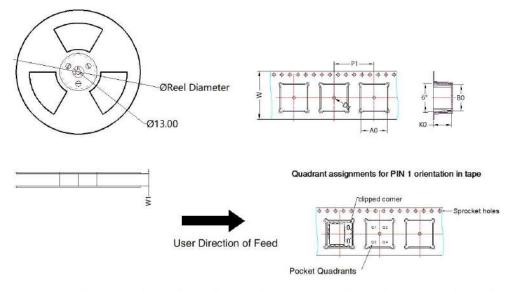
$$R_T(k\Omega) = \frac{12}{V_O - 0.6}$$

Notes: 1.  $R_1$ : Resistance of Trim; Vout: The trim up voltage; 2. If  $R_1 = \infty$  or Trim pin open, Vout = 0.6 VDC.

# **Dimensions and Recommended Layout**



# Tape and Reel Info



Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Clipped corner Quadrant
SK12MT-6A	SMD	17	340	330.0	24.4	12.95	12.95	9.1	20	24	Q2

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#### Notes:

- The maximum capacitive load offered were tested at input voltage range and full load;
- Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25℃, humidity<75%RH with nominal input voltage, 5VDC output voltage and rated output load;
- All index testing methods in this datasheet are based on company corporate standards;
- We can provide product customization service, please contact our technicians directly for specific information;
- 5. Products are related to laws and regulations: see "Features" and "EMC";
- 6. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.